

DM74ALS00A Quad 2-Input NAND Gate

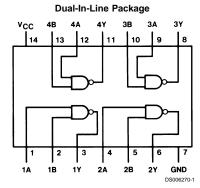
General Description

This device contains four independent gates, each of which performs the logic NAND function.

Features

- Switching specifications at 50 pF
- Switching specifications guaranteed over full temperature and V_{CC} range
- Advanced oxide-isolated, ion-implanted Schottky TTL process
- Functionally and pin for pin compatible with Schottky and low power Schottky TTL counterpart
- Improved AC performance over Schottky and low power Schottky counterparts

Connection Diagram



Order Number DM74ALS00M, DM74ALS00SJ or DM74ALS00N See Package Number M14A, M14D or N14A

Function Table

 $Y = \overline{AB}$

Inputs		Output		
Α	В	Υ		
L	L	Н		
L	Н	Н		
Н	L	Н		
Н	Н	L		

H = High Logic Level L = Low Logic Level **Absolute Maximum Ratings** (Note 1)

Storage Temperature Range

 -65°C to $+150^{\circ}\text{C}$

Supply Voltage Input Voltage

7V Operating Free Air Temperature Range

Typical θ_{JA} N Package M Package

86.5°C/W 116.0°C/W

DM74ALS 0°C to +70°C

Recommended Operating Conditions

Symbol	Parameter		Units		
		Min	Nom	Max	
V _{cc}	Supply Voltage	4.5	5	5.5	V
V _{IH}	High Level Input Voltage	2			V
V _{IL}	Low Level Input Voltage			0.8	V
I _{OH}	High Level Output Current			-0.4	mA
I _{OL}	Low Level Output Current			8	mA
T _A	Free Air Operating Temperature	0		70	°C

Note 1: The "Absolute Maximum Ratings" are those values beyond which the safety of the device cannot be guaranteed. The device should not be operated at these limits. The parametric values defined in the "Electrical Characteristics" table are not guaranteed at the absolute maximum ratings. The "Recommended Operating Conditions" table will define the conditions for actual device operation.

Electrical Characteristics

over recommended operating free air temperature range. All typical values are measured at V_{CC} = 5V, T_A = 25°C.

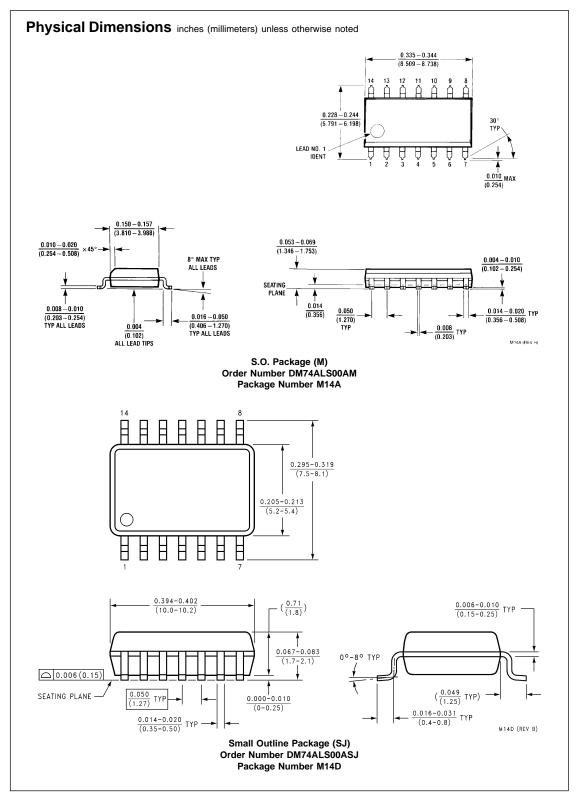
Symbol	Parameter	Conditions		Min	Тур	Max	Units
V _{IK}	Input Clamp Voltage	V _{CC} = 4.5V, I _I = -18 mA				-1.5	V
V _{OH}	High Level Output	$I_{OH} = -0.4 \text{ mA}$		V _{CC} - 2			V
	Voltage	V _{CC} = 4.5V to 5.5V					
V _{OL}	Low Level Output	V _{CC} = 4.5V	74ALS		0.35	0.5	V
	Voltage		I _{OL} = 8 mA				
I _I	Input Current at Max	V _{CC} = 5.5V, V _{IH} = 7V				0.1	mA
	Input Voltage						
I _{IH}	High Level Input Current	V _{CC} = 5.5V, V _{IH} = 2.7V				20	μA
I _{IL}	Low Level Input Current	V _{CC} = 5.5V, V _{IL} = 0.4V				-0.1	mA
Io	Output Drive Current	V _{CC} = 5.5V	V _O = 2.25V	-30		-112	mA
I _{cc}	Supply Current	V _{CC} = 5.5V	Outputs High		0.43	0.85	mA
			Outputs Low		1.62	3	mA

Switching Characteristics

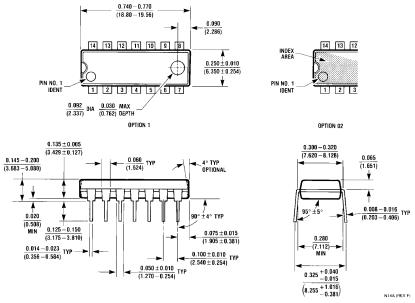
over recommended operating free air temperature range (Note 2)

Symbol	Parameter	Conditions	DM74A	DM74ALS00A	
			Min	Max	
t _{PLH}	Propagation Delay Time	V _{CC} = 4.5V to 5.5V	3	11	ns
	Low to High Level Output	$R_L = 500\Omega$			
t _{PHL}	Propagation Delay Time	C _L = 50 pF	2	8	ns
	High to Low Level Output				

Note 2: See Section 5 for test waveforms and output load.



Physical Dimensions inches (millimeters) unless otherwise noted (Continued)



Molded Dual-In-Line Package (N) Order Number DM74ALS00AN Package Number N14A

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Fairchild Semiconductor Corporation Americas

Tel: 1-888-522-5372

Customer Response Center

www.fairchildsemi.com

Fairchild Semiconductor Europe

Fax: +49 (0) 1 80-530 85 86

Email: europe.support@nsc.com
Deutsch Tel: +49 (0) 8 141-35-0
English Tel: +44 (0) 1 793-85-68-56
Italy Tel: +39 (0) 2 57 5631

Fairchild Semiconductor Hong Kong Ltd. 13th Floor, Straight Block, Ocean Centre, 5 Canton Rd. Tsimshatsui, Kowloon

Hong Kong Tel: +852 2737-7200 Fax: +852 2314-0061 National Semiconductor Japan Ltd.

Tel: 81-3-5620-6175 Fax: 81-3-5620-6179