



DMN2112SN

N-CHANNEL ENHANCEMENT MODE FIELD EFFECT TRANSISTOR

Features

- Low On-Resistance
- Ideal for Notebook Computer, Portable Phone, PCMCIA Cards, and Battery Powered Circuits
- Lead Free By Design/RoHS Compliant (Note 2)
- Qualified to AEC-Q101 Standards for High Reliability
- ESD Protected Gate
- "Green" Device (Note 3)

Mechanical Data

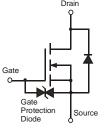
- Case: SC59
- Case Material Molded Plastic, "Green" Molding Compound. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminals: Finish Matte Tin annealed over Copper leadframe. Solderable per MIL-STD-202, Method 208
- Terminal Connections: See Diagram
- Marking Information: See Page 3
- Ordering & Date Code Information: See Page 3
- Weight: 0.014 grams (approximate)

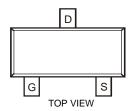






TOP VIEW





EQUIVALENT CIRCUIT Pin Out Configuration

Maximum Ratings @T_A = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Drain-Source Voltage	V _{DSS}	20	V
Gate-Source Voltage Continuous	V _{GSS}	± 8	V
Drain Current Continuous Pulsed	I _D	1.2 4.0	А

SC59

Thermal Characteristics @TA = 25°C unless otherwise specified

Characteristic	Symbol	Value	Units
Total Power Dissipation	P_d	500	mW
Thermal Resistance, Junction to Ambient	$R_{ hetaJA}$	250	°C /W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150	°C

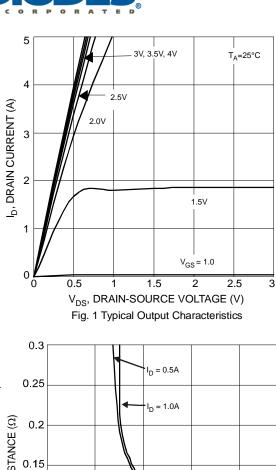
Electrical Characteristics @T_A = 25°C unless otherwise specified

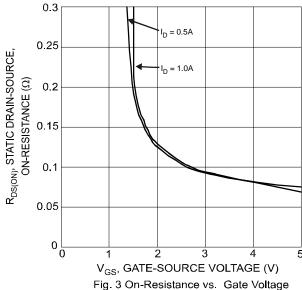
Characteristic	Symbol	Min	Тур	Max	Unit	Test Condition
OFF CHARACTERISTICS (Note 1)			-			
Drain-Source Breakdown Voltage	BV _{DSS}	20	_	_	V	$V_{GS} = 0V, I_D = 250\mu A$
Zero Gate Voltage Drain Current @ $T_j = 25$ °C	I _{DSS}		_	10	μΑ	$V_{DS} = 20V, V_{GS} = 0V$
Gate-Body Leakage	I _{GSS}	_	_	± 10	μΑ	$V_{GS} = \pm 8V$, $V_{DS} = 0V$
ON CHARACTERISTICS (Note 1)			-	•		
Gate Threshold Voltage	$V_{GS(th)}$	0.5	_	1.2	V	$V_{DS} = 10V, I_D = 1.0mA$
				0.10		$V_{GS} = 4.5V, I_D = 0.5A$
Static Drain-Source On-Resistance	R _{DS} (ON)	_	_	0.14	Ω	$V_{GS} = 2.5V, I_D = 0.5A$
				0.25		$V_{GS} = 1.5V, I_D = 0.1A$
Forward Transfer Admittance	$IY_{fs}I$	_	4.2	_	S	$V_{DS} = 10V, I_{D} = 0.5A$
Diode Forward Voltage		_	0.8	1.1	V	$V_{GS} = 0V$, $I_S = 1A$
Diode Forward Voltage V_{SD} — 0.8 1.1 $V_{GS} = 0V$, $I_S = 1A$ DYNAMIC CHARACTERISTICS						
Input Capacitance	Ciss	_	220	_	pF	V 40V V 0V
Output Capacitance		_	120	_	pF	$V_{DS} = 10V, V_{GS} = 0V$ - f = 1.0MHz
Reverse Transfer Capacitance	C_{rss}		45	_	pF	1 = 1.0W112
SWITCHING CHARACTERISTICS						
Turn-On Delay Time	t _{D(ON)}	_	10	_	ns	
Turn-Off Delay Time		_	75	_	ns	$V_{DD} = 5V, I_D = 0.5A,$
Turn-On Rise Time		_	15	_	ns	$V_{GS} = 10V$, $R_{GEN} = 50\Omega$
Turn-Off Fall Time	t _f	_	65	_	ns	

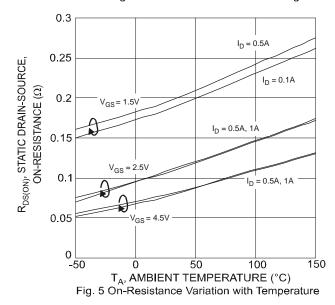
Notes:

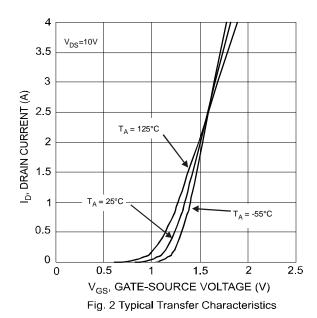
- 1. Pulse width $\leq 300 \mu s$, duty cycle $\leq 2\%$.
- 2. No purposefully added lead.
- 3. Diodes Inc.'s "Green" Policy can be found on our website at http://www.diodes.com/products/lead_free/index.php.

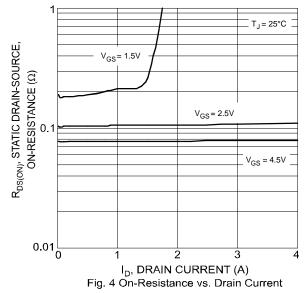


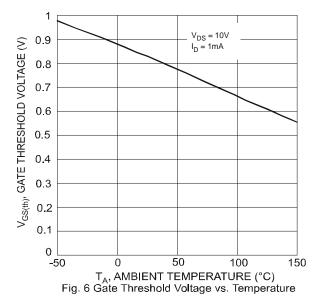




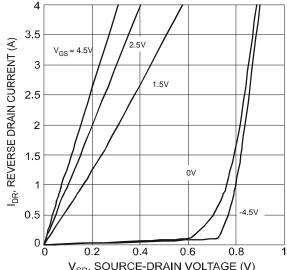




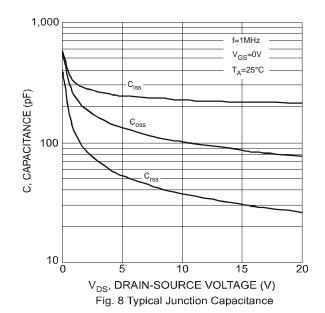








 $V_{\rm SD}$, SOURCE-DRAIN VOLTAGE (V) Fig. 7 Reverse Drain Current vs. Source-Drain Voltage

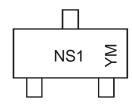


Ordering Information (Note 4)

Part Number	Case	Packaging
DMN2112SN-7	SC59	3000/Tape & Reel

Notes: 4. For packaging details, go to our website at http://www.diodes.com/datasheets/ap02007.pdf.

Marking Information

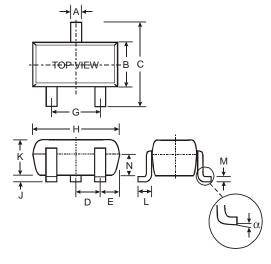


NS1 = Product Type Marking Code YM = Date Code Marking Y = Year ex: T = 2006 M = Month ex: 9 = September

Date Code Key

Year	20	07	20	08	20	09	20	10	20	11	20	12
Code	l	J	\	/	V	٧)	<	`	Y	2	7
Month	Jan	Feb	Mar	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	0	N	D

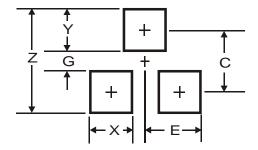
Package Outline Dimensions



SC59					
Dim	Min	Max			
Α	0.35	0.50			
В	1.50	1.70			
С	2.70	3.00			
D	0.0	95			
Е	_	_			
G	1.90				
Н	2.90	3.10			
J	0.013	0.10			
K	1.00	1.30			
L	0.35	0.55			
M	0.10	0.20			
N	0.70	0.80			
α	0°	8°			
All Di	All Dimensions in mm				



Suggested Pad Layout



Dimensions	Value (in mm)
Z	4.0
G	1.2
X	0.9
Υ	1.4
С	2.6
E	0.95

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