Α3

QWIK&LOW BOARD IN DETAIL

The Qwik&Low board was designed by the author, with creative insights from Rick Farmer who developed the board layout and from Bill Kaduck and Dave Cornish of MICRODESIGNS who are producing the board. The Gerber files for the board artwork are freely available from www.qwikandlow.com along with the schematic and parts list for the board.

What follows are a few comments to explain some of the circuitry. The UART circuitry shown in the top left of Figure A3-1 includes an (unpopulated) 3-pin header, H1. The cuttable link on the back of the board between the pins labeled **PC** and **RX** provide the default connection. If a user wants to add a transducer to the board that has a UART output, the link between the **PC** and **RX** pins is cut, a 3-pin header is added to the board, and the UART output from the new device is connected to the **RX'** pin of the header (using #30 wirewrap wire). Then a jumper (i.e., the 100-mil shunt, H2X, in the parts list of Figure A3-2) can be used to connect **PC** to **RX** for downloading user code. The jumper can be moved between **RX'** and **RX** to run the user code.

The **RX'** pin is connected to a $100k\Omega$ pull-up resistor, R4, to ensure that the **RX** input does not float. The input is pulled high to match a transducer with a normal idlehigh UART output. In this case, the application program must reinitialize the UART so that **RXDTP** = 0 in the **BAUDCON** register. This undoes QwikBug's inversion of

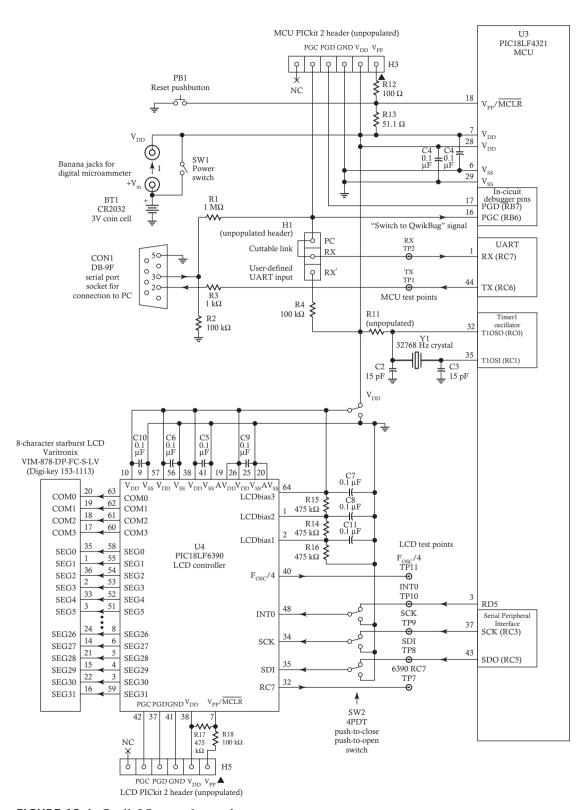


FIGURE A3-1 Qwik&Low schematic

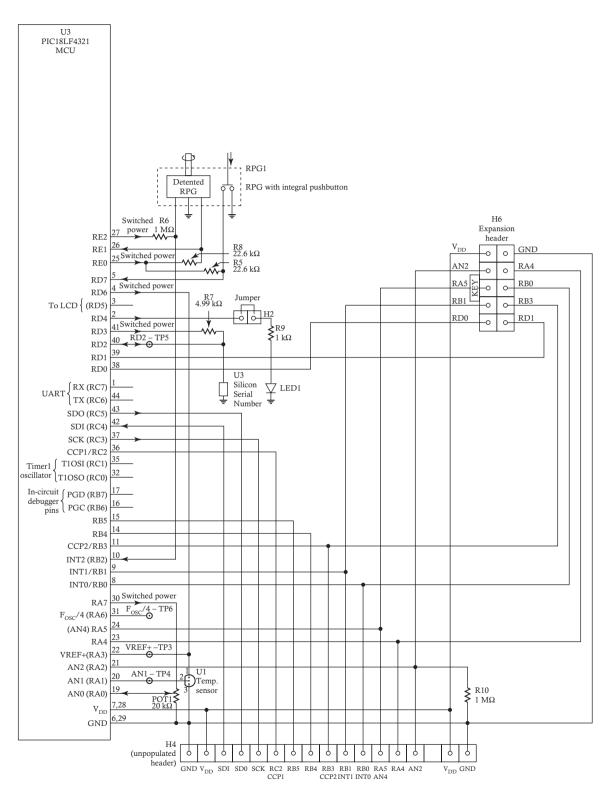


FIGURE A3-1 (continued)

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Total Cost	00'9	0.30	06.0	0.18	0.18	60'0	0.18	60.0	0.18	0.36	0.27		1.05	3.37	1.52	3.27	17.94	88.0	7.86	2.07	2.72	2.92	1.04	0.25	2.96	2.44		0.10	0.12			65'0	0.34	2.19	1.97	0.52	23.47	41.41
Cost Each		0.15	0.10	60.0	60.0	60.0	60.0	60.0	60.0	60.0	60.0		1.05	3.37	1.52	3.27	nents =	0.38	2.86	2.07	2.72	2.92	1.04	0.25	1.48	2.44		0.10	0.12			65.0	0.34	2.19	1.97	0.13	nents =	Total =
Manufacturer's Part Number		VJ1206A150JXACW1BC	CC1206KRX7R9BB104	RC1206FR-07100RL	RC1206FR-071KL	RC1206FR-074K99L	RC1206FR-0722K6L	RC1206FR-0751K1L	RC1206FR-07100KL	RC1206FR-07475KL	RC1206FR-071ML		ABS25-32.768KHZ-T	PIC18LF4321-I/PT	DS2401P+	PIC18LF6390-I/PT	Back of board components	LN28RALXU	AD22103KTZ	EVL-HFAA06B24	5747844-4	VIM-878-DP-FC-S-LV	BH32T-C	CR2032	108-0740-001	TL4201EEYA		4-103239-0	SPC02SYAN			AWHW10G-0202-T-R	KSA0M211 LFT	EC11G1524402	108-0051-EVX	SJ-5003 (BLACK)	Remaining components =	
Manufacturer	PCBCART	BC Comp.	Yageo	Yageo	Yageo	Yageo	Yageo	Yageo	Yageo	Yageo	Yageo		Abracon	Microchip	Dallas Semi.	Microchip		Panasonic	Analog Dev.	Panasonic	Tyco AMP	Varitronix	MPD	Panasonic	Johnson	E-Switch		Tyco AMP	Sullins			Assmann	Cannon	ALPS	Mountain Sw.	3M		
Part Description	Qwik&Low board	15 pf 50V NPO ceramic capacitor	0.1uF 50V X7R ceramic capacitor	100 ohm 1/4W 1% thick film resist.	1.00K 1/4W 1% thick film resistor	4.99K 1/4W 1% thick film resistor	22.6K 1/4W 1% thick film resistor	51.1K 1/4W 1% thick film resistor	100K 1/4W 1% thick film resistor	475K 1/4W 1% thick film resistor	1.00M 1/4W 1% thick film resistor	(leave this unpopulated)	32768 Hz watch crystal	Microcontroller	Silicon Serial Number	LCD controller		Red LED, through-hole mounting	Temperature sensor	20K Thumbwheel POT	Female DB-9 socket	8-char starburst LCD	CR2032 coin cell holder	CR2032 coin cell	Nickel plated banana jack	4PDT push-on, push-off switch	(leave this unpopulated)	Male straight 2-pin header	100 mil shunt	(leave these unpopulated)	(leave this unpopulated)	Male shrouded 5x2 pin header	Sealed pushbutton switch	Detented RPG-PB switch comb.	SPDT toggle switch	Four rubber bumpers for feet		
Footprint		1206	1206	1206	1206	1206	1206	1206	1206	1206	1206	1206		TQFP-44	1S0C-6	TQFP-64		T-1	T0-92	POT_HFAA		2" x 1"			1/4" Dia hole		SIP100_3P	SIP100_2P		SIP100_6P	SIP100_17P				SIP100_3P			
Designation		C2,3	C1,4,5,6,7,8,9,10,11	R12,18	R3,9	R7	R5,8	R13	R2,4	R14,15,16,17	R1,6,10	R11	۲۱	U3	U2	U4		LED1	U1	POT1	CON1	LCD1	BT1	BT1X		SW2	H1	Н2	H2X	H3,5	H4	9Н	PB1	RPG1	SW1			
Distributor		Digi-Key	Digi-Key	Digi-Key		Digi-Key	Digi-Key	Digi-Key		Digi-Key	Digi-Key	Digi-Key	Digi-Key		Digi-Key	Digi-Key		Digi-Key	Digi-Key	Digi-Key	Digi-Key	Digi-Key	Digi-Key	Digi-Key	Digi-Key	Digi-Key	Digi-Key	Digi-Key			Digi-Key		Allied		Mouser	Mouser		
Order Number		BC1305CT	311-1179-1	311-100FRCT	311-1.00KFRCT	311-4.99KFRCT	311-22.6KFRCT	311-51.1KFRCT	311-100KFRCT	311-475KFRCT	311-1.00MFRCT		535-9166-1	PIC18LF4321-I/PT	DS2401P+	PIC18LF6390-I/PT		P605	AD22103KTZ	P4D2203	A32117	153-1113	BH32T-C	P189	J147	EG1874	A26512-40	A26512-40	59001	A26512-40	A26512-40	нкр10н	855-0017	688-EC11G1524402	108-0051-EVX	517-SJ-5003BK		
YlitnauQ	1	2	6	5	2	1	2	1	7	4	3		1	1	1	1		1	1 ,	1	1	1	1	1	2	1	1	1	1	2 ,	1	1	1	1	1	4		

FIGURE A3-2 Owik&Low parts list

the **RX** input. If a transducer is used with a UART output that idles low, the R4 resistor should be removed from the board.

One interesting addition to the board is a 12-key (0–9, *, #) keypad with an 80¢, 14-pin PIC16F505 microcontroller providing:

- The decoding of the keypad.
- A bit-banged UART interface.

The CPU clock of the PIC16F505 is 1 MHz \pm 2%. This is sufficiently accurate so that if an output pin is changed appropriately at intervals of

$$1,000,000/19,200 = 52.08333 \,\mu s \approx 52 \,\mu s$$

then the MCU will read this serial input exactly as it would from a 19,200 baud UART.

The Timer1 crystal oscillator circuitry shown in Figure A3-1 immediately below the UART circuitry includes pads labeled R11. The intent is to make it possible to add a high-impedance (e.g., $\approx 3~\text{M}\Omega$) resistor, to try to get the Timer1 oscillator to run reliably with the

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# pragma config LPT10SC = ON
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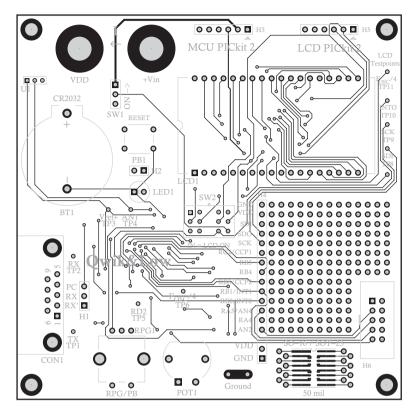
configuration choice. This choice, the reliability of which has not been thoroughly explored, would decrease the current draw of the Timer1 oscillator and the Timer1 counter from about 6 μ A to 1 μ A. That low-power oscillator configuration is meant to run with a 5 V supply, not the 3 V supply of the Qwik&Low board. A Microchip application engineer intimately involved with the oscillator design suggested this pull-up resistor modification.

The unpopulated H4 header provides test points for the pin of a scope probe, to test user-generated outputs (e.g., for a pulse-width measurement on the **RC2** or **RB0** pin). The unpopulated header also provides solder points for an add-on part installed on the surface-mount pads located on the front or the back of the board. Point-to-point wiring with #30 wirewrap wire can produce a clean job of the addition. Be sure to remove any solder flux, especially if water-soluble (i.e., conductive) flux is used.

The prototype area on the board allows DIP parts and discrete parts to be added easily. For surface-mount parts that do not fit on the available surface-mount patterns (e.g., an SOIC part with up to 28 pins), consider the use of one of the surface-mount-to-DIP adapters available from www.beldynsys.com.

The test points dispersed over the board are designed for the pin probe of a scope. $F_{OSC}/4$, the CPU clock for each of the PIC microcontrollers on the board, allows a user to gauge the awake/sleep behavior of each chip under varying circumstances.

An output pin on the MCU can be employed in a user program to monitor time intervals of interest. In like manner, one of the few free output pins of the LCD controller, **RC7**, can be monitored. TP7 is such a test point, located near the right edge of the board below TP8. It is unlabeled (but can be probed) on the front of the board. It is labeled on the back of the board. The intent is to be unobtrusive for normal use of the board, but available for users who wish to explore modifications to the LCD controller's program code.



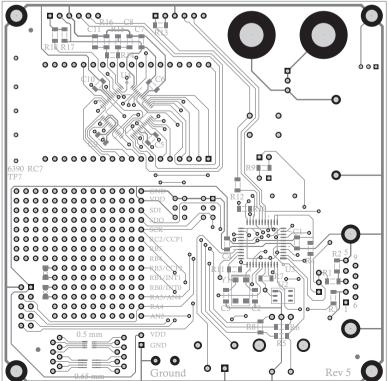


FIGURE A3-3 Front and back of Qwik&Low board

The parts list includes the same Digi-Key part number, A26512-40, for all of the single-in-line (SIP) headers, H1, H2, H3, H4, and H5. This part is actually a 40-pin strip that is designed to be cut with diagonal cutters into headers having the desired number of pins.

The parts list shows the Qwik&Low board as being manufactured by PCBCART (www.pcbcart.com), a vendor for having low-cost, high-quality boards made in China with a nominal two-week delivery schedule. The \$6.00 price shown in Figure A3-2 was the approximate unit price with an order for 30 boards. The Gerber files on the www.qwikandlow.com website can be used to place an order for boards. However, proceed cautiously! As the artwork of Figure A3-3 for the front and the back of the board illustrates, the surface-mount parts for the back of the board, particularly the two PIC microcontrollers, call for considerable experience in dealing with fine-pitch parts. Also, when completed, a PICkit 2 programmer must be available to program the two chips. Before undertaking such a venture, be sure to consider the alternative of purchasing a built and tested board from www.microdesignsinc.com.

Chris Bruhn and Peter Ralston have developed a Performance Verification program, PV.c, available from the www.qwikandlow.com website. This program can be compiled, loaded via QwikBug, and run. It initially sends the serial number from the DS2401 chip (Chapter Fifteen) to the QwikBug console, verifying both the chip and the serial interface. It then runs the LCD display through four quick tests before moving on to display the temperature, and to verify the operation of the Timer1 oscillator and the operation of the potentiometer/ADC combination. If the Timer1 oscillator is working correctly, the middle number on the display will increment every second. The LED also blinks every second. As the potentiometer is turned from full CCW to full CW, the right-hand number changes from 00 to FE or FF. Turning the RPG turns on individual segments of the LCD, verifying that no adjacent pins of the display are shorted together. Turning the RPG clockwise or counterclockwise also increments/decrements a number sent to the QwikBug console. The LED is turned on in response to the pressing of the RPG's pushbutton. The current draw of the board while running this program (with the LED jumper removed and the LCD switched off) is about 20 µA.