

Mfrs. List No.	Pots/ Package	Wiper Memory	No. of Positions	Resistance	Power Supply	Control Interface	Pins
DS1804-50	1	NON-VOLATILE	100-LIN	50K	3V, 5V	INC/DECREMENT	8
DS1804-100	1	NON-VOLATILE	100-LIN	100K	3V, 5V	INC/DECREMENT	8
DS1806-10	6	VOLATILE	64-LIN	10K	3V, 5V	3-WIRE ADDRESSABLE	20
DS1806-50	6	VOLATILE	64-LIN	50K	3V, 5V	3-WIRE ADDRESSABLE	20
DS1806-100	6	VOLATILE	64-LIN	100K	3V, 5V	3-WIRE ADDRESSABLE	20
DS1807	2	VOLATILE	64-LOG	50K	3V, 5V	2-WIRE ADDRESSABLE	14
DS1844-010	4	VOLATILE	64-LIN	10K	3V, 5V	2-WIRE ADDRESSABLE	20
						5-WIRE SERIAL	20
DS1844-050	4	VOLATILE	64-LIN	50K	3V, 5V	2-WIRE ADDRESSABLE	20
						5-WIRE SERIAL	20
DS1844-100	4	VOLATILE	64-LIN	100K	3V, 5V	2-WIRE ADDRESSABLE	20
						5-WIRE SERIAL	20
DS1844S-010	4	VOLATILE	64-LIN	10K	3V, 5V	2-WIRE ADDRESSABLE	20
						5-WIRE SERIAL	20
DS1844S-050	4	VOLATILE	64-LIN	50K	3V, 5V	2-WIRE ADDRESSABLE	20
						5-WIRE SERIAL	20
DS1844S-100	4	VOLATILE	64-LIN	10K	3V, 5V	2-WIRE ADDRESSABLE	20
						5-WIRE SERIAL	20
DS1866	1	VOLATILE	8-LOG	10K	3V, 5V	3-INPUT PARALLEL	8

Order Code	1+	10+	100+	250+
670-881				
670-893				
670-935				
670-947				
670-959				
670-900				
300-1258				
300-1260				
300-1271				
SMD 300-1283				
SMD 300-1295				
SMD 300-1301				
670-911				



Pins	Description
9C102 8	CMOS Digitally Controlled Potentiometer. Max. resistance 1kΩ. (99 Resistive Elements)
9C103 8	CMOS Digitally Controlled Potentiometer. Max. resistance 10kΩ
9C103 8	CMOS Digitally Controlled Potentiometer. Max. resistance 10kΩ
9C104 8	CMOS Digitally Controlled Potentiometer. Max. resistance 100kΩ
9C104 8	CMOS Digitally Controlled Potentiometer. Max. resistance 100kΩ
9C503 8	CMOS Digitally Controlled Potentiometer. Max. resistance 50kΩ
9C503 8	CMOS Digitally Controlled Potentiometer. Max. resistance 50kΩ
9241 20	CMOS, Quad, Digitally Controlled Potentiometer. All pots are 10kΩ
9313 8	CMOS Digitally Controlled Potentiometer. Max. resistance 10kΩ. (31 Resistive Elements)
9313 8	CMOS Digitally Controlled Potentiometer. Max. resistance 10kΩ. (31 Resistive Elements)
9315 8	CMOS Digitally Controlled Potentiometer. Max. resistance 10kΩ. (31 Resistive Elements)
9315 8	CMOS Digitally Controlled Potentiometer. Max. resistance 10kΩ. (31 Resistive Elements)
9316 14	CMOS Digitally Controlled Potentiometer. Max. resistance 10kΩ. (31 Resistive Elements)
9316 14	CMOS Digitally Controlled Potentiometer. Max. resistance 10kΩ. (31 Resistive Elements)

Mfrs. List No.	Order Code	1+	10+	100+	250+	500+
X9C102P	204-018					
X9C103P	203-993					
X9C103S	SMD 631-450					
X9C104P	204-006					
X9C104S	SMD 631-462					
X9C503P	270-751					
X9C503S	SMD 631-474					
X9241WP	562-403					
X9313WP	270-740					
X9313WS	SMD 788-004					
X9315WP	300-6062					
X9315WS	SMD 300-6050					
X9316WP	300-6086					
X9316WS	SMD 300-6074					

## Touch Memory/ID



Mfrs. List No.	Description
1921 -	Thermochron button with digital thermometer measuring in 0.5°C increments and temperature conversion accuracy of ±1°C. Includes real-time clock, programmable high/low alarm trip points. Automatically wakes up and measures temperature at user programmable intervals. Records up to 2048 consecutive measurements (F5MICROCAN)
1982 -	1Kbit Add-Only Memory with unique 48-bit serial number, 8-bit CRC and 8-bit family code. 1K-bit EPROM is user programmable, partitioned into four 256-bit pages, each can be permanently write-protected. (F5MICROCAN)
1990 -	Touch Memory button with unique 48-bit serial number, 8-bit family code, 8-bit CRC tester (IND TEMP)
1991 -	Touch Memory button with serial number, Non-Volatile scratched RAM and 3 independent password secured RAM areas
1992 -	Touch Memory button with serial number and internal 128 bytes of Non-Volatile RAM organized in 4 areas of 32 bytes each and a scratchpad of 32 bytes
1993 -	Touch Memory button with serial number, with four times the storage capacity of the DS1992
1994 -	Touch Memory button with serial number and four times the storage capacity of the DS1992. Additionally contains a Real Time Clock, interval timer, cycle counter, and alarm features for counters
1995 -	Touch Memory button with 16Kbits of read/write non-volatile memory organised as 64 pages of 32 bytes and a 256-bit scratchpad. (F5MICROCAN)
1996 -	Touch Memory button with 64Kbits of read/write non-volatile memory organised as 64 pages of 32 bytes and a 256-bit scratchpad. (F5MICROCAN)
2401 3/TO-92	Silicon serial number, 64-bit ROM includes unique 48-bit serial number, 8-bit CRC and 8-bit Family Code. Data transferred serially via 1-Wire protocol
2401 4/SOT-223	Silicon serial number, 64-bit ROM includes unique 48-bit serial number, 8-bit CRC and 8-bit Family Code. Data transferred serially via 1-Wire protocol
2405 3/TO-92	Addressable Switch for assigning electronically readable identification to a particular node controlled by open drain N-channel MOSFET switched by matching 64-bit registration number
2405 4/SOT-223	Addressable Switch for assigning electronically readable identification to a particular node controlled by open drain N-channel MOSFET switched by matching 64-bit registration number
2502 3/TO-92	1K-bit Add-Only Memory consists of registration number with unique 48-bit serial number, 8-bit CRC and 8-bit Family Code. 1K-bit EPROM is user programmable.
9092 -	Touch Memory Probe to provide data transfer to and from Touch Memory button
9093 -	MicroCan holder for the Touch Memory button - Black
9093 -	MicroCan holder for the Touch Memory button - Blue
9093 -	MicroCan holder for the Touch Memory button - Green
9093 -	MicroCan holder for the Touch Memory button - Red
9093 -	MicroCan holder for the Touch Memory button

Mfrs. List No.	Order Code	1+	10+	100+	250+	500+
DS1921L-F51	333-0096					
DS1982-F5	333-0126					
DS1990A-F5	790-461					
DS1991L-F5	796-074					
DS1992L-F5	796-086					
DS1993L-F5	670-728					
DS1994L-F5	796-098					
DS1995L-F5	333-0102					
DS1996L-F5	333-0114					
DS2401	670-730					
DS2401Z	SMD 670-741					
DS2405	670-753					
DS2405Z	SMD 670-765					
DS2502	670-777					
DS9092	790-473					
DS9093A	315-3320					
DS9093AB	315-3332					
DS9093AG	315-3344					
DS9093AR	315-3356					
DS9093F	790-485					